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FIG.1

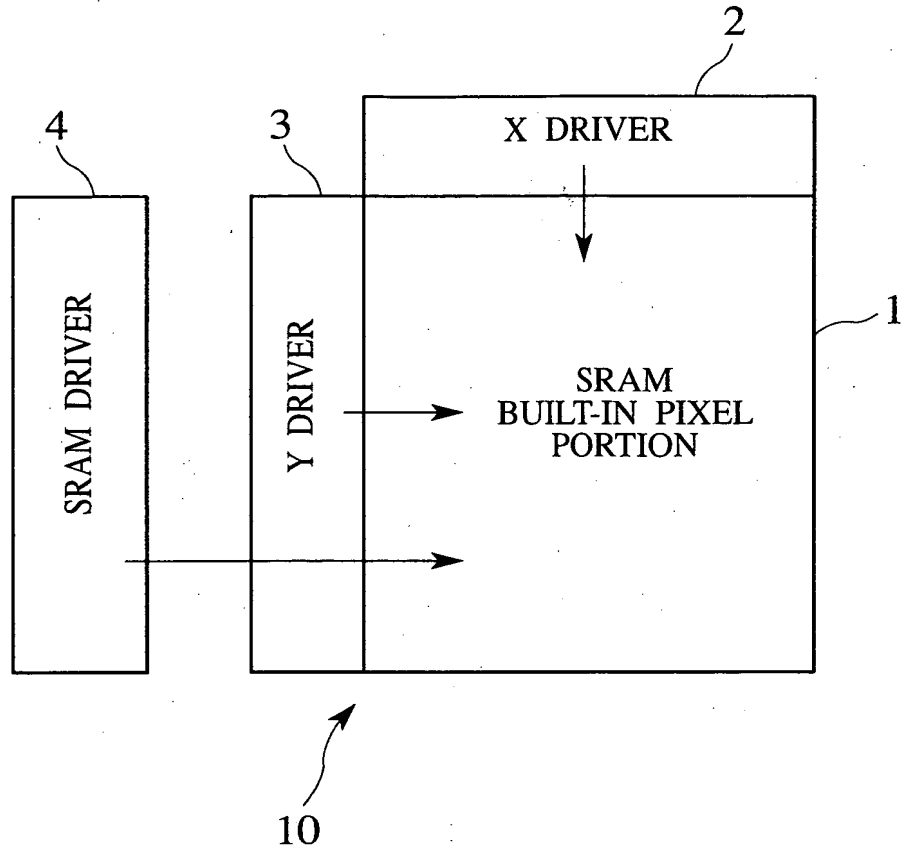


FIG.2

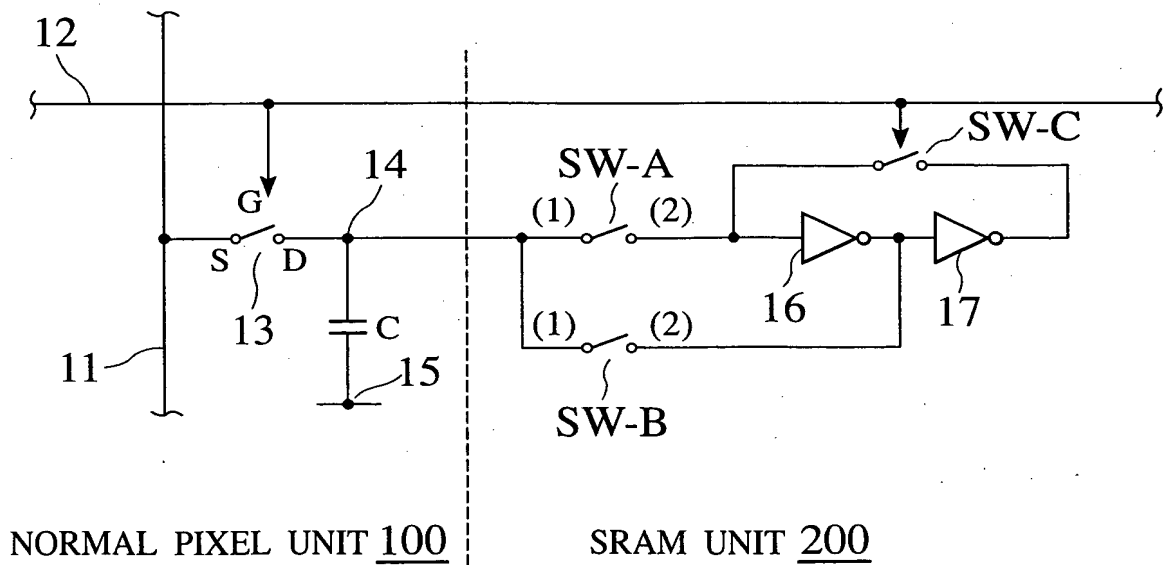




FIG.4

| CIRCUIT CONFIGURATION   | POWER SOURCE VOLTAGE   |                       | SRAM DRIVE                       |
|---|------------------------|-----------------------|----------------------------------|
|   | VDD                    | VSS                   |                                  |
| <ul style="list-style-type: none"> <li>• X DRIVER</li> <li>SHIFT REGISTER</li> <li>DATA LATCH</li> <li>GRADATION VOLTAGE SELECTION UNIT</li> <li>SIGNAL LINE OUTPUT UNIT</li> </ul> | XVDD                   | GND                   | UNREQUIRED                       |
| <ul style="list-style-type: none"> <li>• Y DRIVER</li> <li>SHIFT REGISTER</li> <li>LEVEL SHIFTER</li> <li>SCAN LINE OUTPUT UNIT</li> </ul>  | YVDD<br>YGVDD<br>YGVDD | GND<br>YGVSS<br>YGVSS | REQUIRED<br>REQUIRED<br>REQUIRED |
| <ul style="list-style-type: none"> <li>• SRAM DRIVER</li> <li>SRAM CONTROL SIGNAL GENERATING UNIT</li> <li>SRAM INVERTER POWER SOURCE UNIT</li> </ul>                               | YGVDD<br>SVDD          | YGVSS<br>SVSS         | REQUIRED<br>REQUIRED             |

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FIG.5

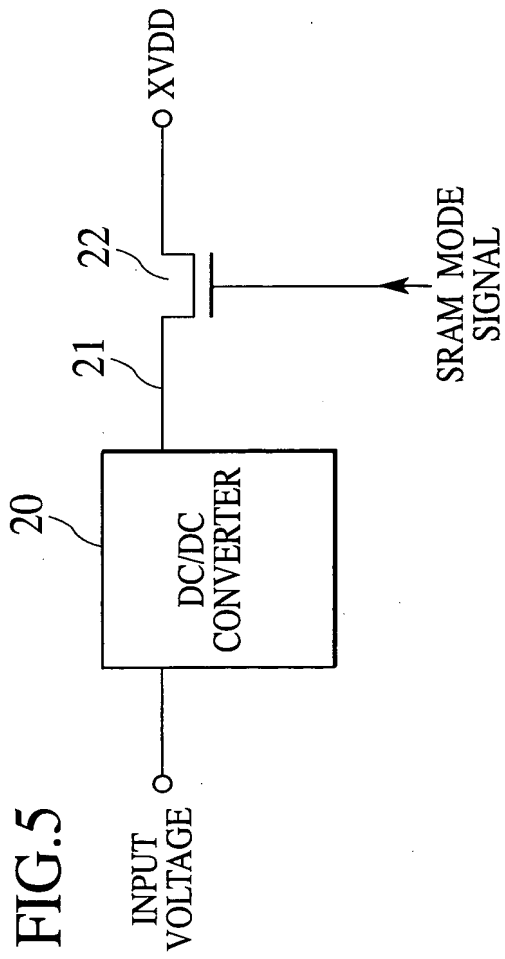


FIG.6

